## Supplementary Information

## **Bioinspired Bio-Voltage Memristors**

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**Supplementary Figure 1** | **Device fabrication and structure**. (a) (Top panel) Schematic of a nanowire memristor device, showing a protein-nanowire thin film sandwiched in a nanogap defined by a pair of Ag electrodes that further connect to Au contacts (for probe access). (Bottom panel) Scanning electron microscopy (SEM) image of a typical as-made nanowire device. Scale bar, 1  $\mu$ m. (b) Zoom-in SEM image of the nanogap region in the device. Note that nanowire film was less visible under SEM. Scale bar, 100 nm. (c) SEM cross-section view of a nanowire film prepared by using the same dropcasting procedure involved in nanowire layers. Scale bar, 1  $\mu$ m. (d) Transmission electron microscopy (TEM) image of a few nanowire layers. Scale bar, 100 nm. (e) Atomic force microscope (AFM) measurement showed a predominance of nanowires with the average diameter of ~2.9\pm0.35 nm, which was consistent with the reported 3 nm diameter of PilA-based filaments<sup>1</sup> and is inconsistent with the 4 nm diameter of OmcS-based filaments.<sup>2,3</sup>

Note that for TEM imaging, few-layer nanowires were chosen for electron transparency. Consequently, the nanowire density revealed was not a close representation of the nanowire packing density in the actual film (~500 nm thick) used in memristors. As was revealed by cross-section SEM image in (c), the protein nanowires assumed a dense packing configuration in the thin film. The tight nanowire-nanowire interface, revealed by molecular dynamics simulations (Supplementary Figure 2), acted more like the grain boundary in inorganic dielectrics.<sup>1</sup> As a result, the nanowire film behaved like a biological dielectric that allowed for standard lithographic patterning (Supplementary Figure 3), which was completely different from previous loose 'nanowire mesh' used in memristors.<sup>4,5</sup>



**Supplementary Figure 2** | **Protein nanowire interfaces**. Simulation details are described in the *Methods*. Recent development has shown the possibility of the coexistence of pili and OmcS nanowire structures,<sup>1-3</sup> although our characterization indicated the predominance of pili nanowires (Supplementary Figure 1e). We modeled different nanowire-nanowire interfaces to provide a comprehensive view. The nanowire-nanowire interface in the simulation involved two nanowire filaments in parallel configurations. (**a**) Representative snapshot of a pili-pili interface and the water-permeable pore (green) from molecular modelling. The blue filament was set as the reference filament, with its principal axis oriented along *x*-axis. The red filament, in parallel with the blue one, was mobile, which could both translate along *y*-axis and rotate along its filament axis. Similar procedure was adopted in the modelling of OmcS-OmcS and OmcS-pili nanowire interfaces. (**b**) Pore radius at each shift distance ( $\Delta x$ ) along *x*-axis for the most stable packing conformation in OmcS-OmcS (green), OmcS-pili (blue), and pili-pili nanowire interfaces (red). The black dashed line indicates the radius of water molecule.



Supplementary Figure 3 | Proof-of-concept demonstration of patterning protein-nanowire film in vertical memristors. (a) Schematics of patterning nanowire film by using standard lithography and lift-off processes. Specifically, (1) ~500 nm thick photoresist (Microposit S1805) was spin-coated on the fabricated vertical Ag-SiO<sub>2</sub>-Pt devices. (2) The vertical device was then exposed (within a window size ~50×50  $\mu$ m<sup>2</sup>) by standard exposure (with alignment) and development processes. (3) The protein-nanowire solution was dropcasted into the exposed window region and naturally dried in the ambient environment. The thickness (~500 nm) was controlled by dropcasting volume (see Methods). (4). A final life-off in acetone yielded Ag-SiO<sub>2</sub>-Pt devices embedded in patterned protein-nanowire film. (b) Zoom-in optical image of a vertical Ag-SiO<sub>2</sub>-Pt device (size ~2×2  $\mu$ m<sup>2</sup>) embedded in a patterned protein-nanowire film.



Supplementary Figure 4 | Initial conduction in *protein-nanowire* planar device. Before deposition of nanowire film in the planar Ag-Ag nanogap (~200 nm), the device showed no conduction (black curve). The nanowire film (500 nm thick) was then formed by dropcasting. Low conductance was observed in the nanowire films (N=10, red curves). The estimated conductivity was ~80 µS/cm which was consistent to previous values measured from wild-type protein nanowires prepared in basic solution.<sup>7</sup>



**Supplementary Figure 5** | **A typical electroforming process in the protein-nanowire memristor**. The device was a planar device (inset) with the gap size ~200 nm. The looped voltage sweep was initially up to 2.0 V, during which a sudden conductance jump was observed (black arrow). The voltage in the following sweeps was gradually reduced, within which the sudden conductance jump still maintained with its threshold voltage gradually reducing until stable switching was established (red curve). The numbers indicate the sweep order.



**Supplementary Figure 6** | **Device statistics**. (a) The average turn-on threshold voltage ( $V_{th}$ ) with respect to the nanogap spacing in protein-nanowire memristors. A total number of 60 devices, with 12 for each spacing, were studied. The error bars represent the standard deviation (s.d.). The average  $V_{th}$  maintained a value between 40-80 mV.  $V_{th}$  is also largely independent of the nanogap size, which is consistent to a filamentary switching mechanism in which the confined switching point generally is independent of the filament length. The inset shows the schematic of the memristive device, with the protein nanowires filled in the nanogap defined by a pair of Ag electrodes. (b) Device yield with respect to the nanogap spacing. All the measurements were performed in the ambient environment (RH ~ 40%).



**Supplementary Figure 7**. Turn-on curve from a typical planar protein-nanowire memristor, showing a steep slope of 0.4 mV/dec.



Supplementary Figure 8 | Pulse width-dependent relaxation time in vertical memristors. (a) Representative conductance relaxations from a vertical protein-nanowire device switched by programming pulses of different widths. The pulse amplitude was kept at 100 mV. The relaxation current was recorded at 10 mV. (b) Average relaxation time with respect to the programming pulse width. The error bars represent the standard deviation (s.d.). The approximately linear increase in relaxation time with respect to pulse width is consistent to other studies.<sup>8</sup> Note that this linear fitting does not pass the origin, because only pulses with width >10 ms can turn on the device (Fig. 2d). The extrapolated 0 ms retention at ~10 ms input pulse width is consistent to the experimental result (Fig. 2d).



Supplementary Figure 9 | Electroforming in vertical protein-nanowire memristors. (a) An electroforming process (red curve) followed by switching *I-Vs* (gray curves) in a vertical device (size ~  $2\times 2$   $\mu$ m<sup>2</sup>) at RH~35%. (b) Statistics of the forming voltages ( $V_f$ ) from 57 devices, showing an average value of 70±19 mV.



**Supplementary Figure 10** | **Device scaling in vertical protein-nanowire memristors**. (a) Representative electroforming process (red curve) followed by 10 consecutive switching *I-Vs* (gray curves) in vertical devices of different sizes ( $20 \times 20$ ,  $10 \times 10$ ,  $5 \times 5$ ,  $2 \times 2 \mu m^2$ ). (b) Statistics of the forming voltages ( $V_f$ , red dots) and turn-on voltages ( $V_{th}$ , blue diamonds) with respect to device sizes (N=5 for each size). The error bars represent the standard deviation (s.d.). The electrical characterizations were performed at RH~35%.



Supplementary Figure 11 | Memristive switching in a  $100 \times 100 \text{ nm}^2$  vertical protein-nanowire device. (a) Optical and SEM images of the vertical Ag-SiO<sub>2</sub>-Pt structure. (b) The electroforming process (red curve) followed by switching *I-V*s in the device (ambient RH ~35%).



Supplementary Figure 12 | Memristor function with vertical device fabricated with protein nanowires harvested from a strain of *G. sulfurreducens* in which the gene for OmcS was deleted. To further clarify whether OmcS-based nanowire filaments are a contributing factor in observed memristive switching (Fig. 2), protein nanowires were harvested from a strain of *G. sulfurreducens* with the gene for OmcS deleted<sup>6</sup> and used to construct vertical device (inset) similar to that in Fig. 2e. The device showed forming (red curve) and memristive switching (gray curves) similar to ones made with wild type protein nanowires (Supplementary Figure 10), indicating that OmcS is not a contributing factor in observed phenomenon.



Supplementary Figure 13 | Electroforming in vertical Ag-SiO<sub>2</sub>-Pt devices treated with ethanolamine. The ethanolamine buffer involved in nanowire purification was removed by dialyzing the nanowire preparation against deionized water (see Methods). To exclude the possibility that ethanolamine residue could have contributed to the sub-100 mV forming and switching, control vertical Ag-SiO<sub>2</sub>-Pt devices (N=5) were dropcasted with the same volume of non-diluted 150 mM ethanolamine (without protein nanowires) and dried. None of the devices showed any switching behaviors (color curves) following typical electroforming steps employed in protein-nanowire devices (Supplementary Figure 9).



Supplementary Figure 14 | Electroforming in protein-nanowire devices *v.s.* a bare pair of Ag electrodes. (a) The average forming voltage ( $V_f$ ) in protein-nanowire devices with respect to the nanogap spacing. The data were from the same 60 devices studied in Supplementary Figure 6. A general trend of increasing  $V_f$  with increasing nanogap spacing was observed, consistent with the field-driven mechanism involved in typical soft breakdown. (b) The average forming voltage ( $V_f$ ) in a bare pair of Ag electrodes (without protein nanowires) with respect to the nanogap spacing. No stable switching was observed at low bias (*e.g.*, <200 mV) afterwards. (Right panel) SEM image showing Ag migration (dashed area) in SiO<sub>2</sub> substrate between a bare pair of Ag electrodes during an electroforming attempt. Scale bar, 100 nm. The error bars in (**a**) and (**b**) represent the standard deviation (s.d.).



Supplementary Figure 15 | Attempted electroforming and switching in bare Ag-SiO<sub>2</sub>-Pt devices. The devices tested (N=6) had all the same structure parameters, but did not have protein nanowires (schematic). The testing was carried out in the same ambient condition (RH~35%). The *I*-*V*s above from a representative device shows an initial forming voltage ~700 mV (red curve), but failed to form reliable switching at smaller bias (*e.g.*, <200 mV). The rest 5 devices showed similar forming voltages of ~600, 550, 450, 550, and 600 mV, but also failed to yield stable low-voltage switching.



Supplementary Figure 16 | Effect of SiO<sub>2</sub> surface property. Both (a) anionic ( $\alpha$ -NH2,  $\omega$ -COOH-terminated polyethylene glycol)) and (b) cationic ( $\omega$ -Amino-terminated poly(ethylene glycol) methyl ether) surfactants were used to change the surface charge states<sup>9,10</sup> and hence surface potential in SiO<sub>2</sub> before depositing protein nanowires. Both devices showed similar bio-voltage switching. The black curves were the initial forming curves. Surface functionalization alone (without protein nanowire) could not yield bio-voltage memristive switching.



**Supplementary Figure 17** | **Control nanowire/nanotube devices**. The same vertical Ag-SiO<sub>2</sub>-Pt memristor structures were embedded in single-walled carbon nanotubes (CNT) and Si-nanowire networks. (a) Devices embedded in CNT initially showed high conduction dominated by CNT transport. (b) Devices embedded in semiconducting Si-nanowire network showed low conduction (*e.g.*, similar to bare device). None of the devices formed bio-voltage switching following the same forming process. The results are consistent with previous studies,<sup>4,5</sup> in which Ag-based memristors using nanowire mesh/network did not yield low-voltage switching. These 'negative' controls indicate that it is not the percolation material structure that contributes to the effect.

The CNTs (#US4101; US Research Nanomaterials, Inc.), with diameters of 1-2 nm similar to protein nanowire, are expected to yield molecular pores<sup>11</sup> in the film. The thin film was made by dropcasting CNT solution prepared in dimethyl sulfoxide (DMSO). The Si nanowires were grown by a nanocluster-catalyzed vapor–liquid–solid method described previously.<sup>12</sup> Contact printing<sup>13</sup> and multiple transfer processes<sup>14</sup> were used to transfer nanowire films onto the Ag-SiO<sub>2</sub>-Pt device.



Supplementary Figure 18 | Moisture-adsorption measurement in thin films. (a) The measurement setup. It includes a gas-purge desiccator cabinet (H42053-0002; Bel-Art) with an inlet and outlet that allow the controlled flow of dry air, a portable hygrometer (Model 8706; REED Instruments) that can real-time monitor the relative humidity (RH) in the cabinet, and a quartz crystal microbalance (QCM; 400C, CH Instruments) that can monitor the thin-film mass. (b) The thin film (*e.g.*, protein nanowires or polyvinylpyrrolidone film) was first deposited on the quartz crystal resonator (top) by dropcasting. The mass sensitivity of the QCM originates from the dependence of the oscillation frequency on the total mass of the metal-coated crystal, including any deposited material. The mass change can be determined by  $\Delta m =$ 

 $-\Delta f \cdot A \cdot \frac{\sqrt{\mu\rho}}{2f_0^2}$ , where  $f_0$ , A,  $\rho$ ,  $\mu$  are resonant frequency of crystal's fundamental mode, area of the gold disk on the crystal, crystal's density (2.684 g·cm<sup>-3</sup>) and shear modulus of quartz (2.947×10<sup>11</sup> g·cm<sup>-1</sup>·s<sup>-2</sup>), respectively.<sup>15</sup> We first determined the mass of the coated thin film ( $W_{\text{film}}$ ) in the ambient environment. Then the resonator was placed in the desiccator cabinet with dry air flowing in to reduce RH to ~0% inside to force out adsorbed moisture in the thin film. During the process, the mass change ( $\Delta W_{film}$ ) that corresponds to the amount of moisture loss in the film, was continuously monitored (reflected by the resonant-frequency change in QCM). (c) Weight change in a protein nanowire film measured with QCM. The red arrow indicates the start of moisture removal by dry air, whereas the blue arrow indicates the start of moisture re-adsorption in the protein nanowire film (at ambient RH~35%).



Supplementary Figure 19 | Relative-humidity (RH) control during device measurements. (a) The actual setup for controlled RH in device testing environment. The higher RH was controlled by tuning the equilibrium vapor pressure of sulfuric acid solution,<sup>16</sup> *i.e.*, the concentration of sulfuric acid in the open container inside the chamber. (b) An example showing the control of RH ~100% in the testing environment.



**Supplementary Figure 20** | **Memristive switching in PVP-Ag device**. (a) Moisture content measured by QCM (Supplementary Figure 18), which showed that a thin PVP film (~1.8  $\mu$ m) adsorbed ~40% weighpercentage moisture. The red arrow indicates when dry air flew in to drive out the moisture, whereas the green arrow indicates the dry-air flow stopped and moisture re-adsorption happened. (b) Forming and switching process in a PVP memristor (inset) in the first five consecutive sweeps. The device never went to a turn-on voltage below 0.5 V. The turn-on voltage featured a large variation with the average value >0.5 V. The nanogap spacing between the pair of Ag electrode was 200 nm. The testing was in the ambient environment with RH ~40%.



Supplementary Figure 21 | Conduction modulation by pulse width in protein-nanowire synapse made from a planar memristor. (Top panel) Schematic of the input pulse train to an activated protein-nanowire memristor. The pulse frequency (f = 10 Hz) or period (T = 100 ms) is fixed, whereas the pulse width ( $\tau$ ) is modulated. (Bottom panel) At  $\tau$ =50 ms, the activated nanowire synapse showed increasing conductance (red curve) or paired pulse facilitation (PPF) with increased number of pulses. Reduced pulse width ( $\tau$ =6 ms) resulted in a decrease in conductance or paired pulse depression (PPD) with increased number of pulses.



**Supplementary Figure 22** | **Dynamic power in neuronal spiking.** (a) Representative (red) integrate-andfire process in an artificial neuron by using (black) bio-voltage pulses (100 mV, 10  $\mu$ s width, 15  $\mu$ s period). Current compliance ( $I_{CC}$ ) can be added by connecting a compliance resistor ( $R_C$ ). (b) Energy consumption (*E*) in the spiking event at different  $I_{CC}$  in our device, compared to estimated values (color dots) from previously reported neurons.<sup>17-21</sup> The data shows that even without current compliance, the energy cost in our device is still close to two orders of magnitude lower than the lowest reported value. (c) To include the energy cost in the incubation period (*i.e.*, leaky current before spiking), the Off currents were measured (the device was reversely biased to prevent turning On). (d) The total energy cost ( $E_{total}$ ) in a spiking event, as the summation of dynamic energy in (b) and energy loss through leaky current in (c) during the incubation period. The error bars in (b) and (d) represent the standard deviation (s.d.).

The initiation of an action potential in biological cells is estimated to consume  $10^7$ - $10^8$  ATP.<sup>22,23</sup> This leads to an estimated 1-10 pJ energy cost (*e.g.*, based on a free energy of ~60 kJ/mol during the hydrolysis of ATP to ADP). Therefore, the energy cost in the bio-voltage neuronal spiking is close to or even below that of a biological cell.



Supplementary Figure 23 | Implementation of bio-voltage neurons in neural network. (a) Previous work has demonstrated efficient pattern classification using a fully memristive neural network integrated with both synapses (by nonvolatile memristors) and neurons (by diffusive memristors).<sup>21</sup> Without losing generality, here we schematically represent the network consisting of a 2×2 synapse matrix and 2 neurons (made from bio-voltage diffusive memristors). The pre-synaptic input vector is first computed by the synapse matrix, forming the post-synaptic signal vector that further feeds into the neuron array. The neuron array integrates the post-synaptic vector (or weighted synaptic signals) to form the output vector. (b) SEM image of a  $10 \times 10$  synapse matrix in a crossbar architecture, each synapse being a nonvolatile memristor made of Ag/SiO<sub>2</sub>/Pt (150nm/35nm/20nm).<sup>24</sup> We used a  $2\times 2$  synapse matrix (red box) from it. (c) Characteristic switching *I*-V from a synapse in the crossbar, showing a set voltage  $\sim 1.8$  V and reset voltage ~ -1.5 V. (d) The weight/conduction in each pre-trained synapse (read by 100 mV), which is further represented in a color map in (e). (f) Spiking amplitude of 100 mV (top) was used for post-synaptic readout, which was subsequently integrated by the neurons (owing to the bio-voltage function) for different outputs (bottom). The energy cost in this reading process was substantially reduced by using bio-voltage neurons, as analyzed before (Supplementary Figure 22). If we could replace the high-voltage (e.g.,  $V_T > 1V$ ) synapses with bio-voltage (e.g.,  $V_T < 100 \text{ mV}$ ) synapses, the entire unsupervised training/learning<sup>21</sup> could be realized with 100 mV spikes to yield substantial power reduction as well.

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